

## **Curriculum Vitae**

### **Maria Gabriella Cimatori**

Maria Gabriella Cimatori received the M.S. degree in Mathematics in 1989 from the University of L'Aquila with score 110/110 cum laude. After graduation she attended the post-graduate Inter-University School of Perugia and the CNR Computational Mathematics School in Naples. Since 1994 she is Researcher in Numerical Analysis at University of L'Aquila where she teaches Numerical Analysis and Mathematics Complements.

Her research activities have been mostly devoted to new spline operators for approximation of functions, for numerical evaluation of Cauchy principal value integrals and for numerical solution of integro-differential equations. Her current research interests are new analytical and numerical methods for modulation algorithms applied to multilevel converters. She is author and co-author of papers published in international journals or in proceedings of international conferences.

In 2018, a paper presented in Japan at the sixth International Conference on Smart Grids (ICSG) of which she is co-author, received the award for the best paper.

She has been member of the Phd Committee on "Systems and methods for the management of electrical and thermal energy from renewable and assimilated sources and for sustainable building" of DISIM Department of University of L'Aquila from 2012 to 2013.

She is a reviewer for some international journals, between them IEEE Transactions on Industrial Electronics. Since 2018 she is component of Scientific Committees of AEIT- Automotive. In particular, in 2019, she has been component of Technical Committee of 111-th 2019 AEIT International Conference in Florence and co-organizer (publicity chair) of 5th International Symposium on Environment Friendly Energies and Application (EFEA 2018) in Rome. In 2016 she has been co-organizer (publication co-chair) of 42th Annual International Conference of the IEEE Industrial Electronics Society (IECON 2016 -IEEE-IES) in Florence.

### **Journal papers**

- [1] C. Buccella; M. G. Cimatori; C. Cecati, Mathematical Proof of a harmonic elimination procedure for multilevel inverters, *Mathematics and Computers in Simulations* (MATCOM, ScienceDirect, 2020 Elsevier), 2021, Vol. 184, p. 69-81.
- [2] C. Buccella; M. G. Cimatori; C. Cecati, Selective Harmonics Elimination Modulation for HVDC Modular Multilevel Converter, 2021 AEIT HVDC International Conference, 27-28 Maggio 2021, Genoa, Italy.

- [3] C. Buccella; M. G. Cimatorni; C. Cecati, General Formula for SHE Problem Solution, *Energies MDPI*, 2020, 13, 3740, p. 1-16.
- [4] C. Buccella; M. G. Cimatorni; C. Cecati, Mathematical Procedure for Harmonic Elimination in CHB Multilevel Inverters with Variable DC Sources, *Springer Nature Switzerland AG 2020, W. Zamboni, G. Petrone (eds.), ELECTRIMACS 2019, Lecture Notes in Electrical Engineering 697, Springer, Selected Papers - Vol. 2, p. 303-315.*
- [5] C. Buccella; C. Cecati; M.G. Cimatorni, Selective Harmonics Elimination for Nine Level Inverter Based on Linear System Solution, 2020 2nd IEEE International Conference on Industrial Electronics for Sustainable Energy Systems (IESES), 1-3 Sept. 2020, Cagliari, Italy.
- [6] C. Buccella; C. Cecati; M.G. Cimatorni; R. Miceli; G. Schettino; V. Castiglia; F. Pellitteri, Harmonic Reduction in CHB 13-level Inverters by PAM Fundamental-Frequency Strategy, 2019 8th International Conference on Renewable Energy Research and Applications (ICRERA), 3-6 Nov. 2019, Brasov, Romania.
- [7] C. Buccella; M. G. Cimatorni; M. Tinari; C. Cecati, Seven-level cascaded inverters for Uninterruptible Power Supply (UPS) applications, 2019 45<sup>th</sup> Annual Conference of the IEEE Industrial Electronics Society (IECON), 14-17 Oct. 2019, Lisbon, Portugal.
- [8] A. M. Saif; C. Buccella; M. G. Cimatorni; C. Cecati, Power Quality Improvement for Chopper-Cell Based Modular Multilevel Converters, 2019 AEIT International Annual Conference (AEIT), 18-20 September 2019, Florence, Italy.
- [9] C. Buccella; M. G. Cimatorni; C. Cecati, A comparison between real-time harmonic elimination procedures for single phase CHB 5-level inverter, 2019 21st European Conference on Power Electronics and Applications (EPE '19 ECCE Europe) , 3-5 September 2019, Genova, Italy.
- [10] C. Buccella, M. G. Cimatorni, M. Tinari, C. Cecati, A New Pulse Active Width Modulation for Multilevel Converters, *IEEE Transactions on Power Electronics*, August 2019, Vol. 34, Issue 8, p. 7221-7229.
- [11] C. Buccella; M. G. Cimatorni; V. Patel, M. Tinari; C. Cecati, Investigation about SHM-PAM procedure for grid connected CHB seven level inverters, 2019 International Conference on Clean Electrical Power (ICCEP) , 2-4 July 2019, Otranto, Italy.
- [12] V. Castiglia; R. Miceli; G. Ala; C. Cecati; C. Buccella; M. G. Cimatorni, A 9-level three-phase multilevel converter with harmonic mitigation and integrated battery balancing, 2019 AEIT International Conference of Electrical and Electronic Technologies for Automotive (AEIT Automotive), 2-4 July 2019, Torino, Italy.

- [13] C. Buccella; M. G. Cimatori; C. Cecati, Low-Frequency Harmonic Elimination Technique in Three Phase Cascaded H-Bridges Multilevel Inverters for Renewable Energy Applications, *International Journal of Smart Grid*, Vol.3, No.1, March 2019.
- [14] C. Buccella, C. Cecati, M. G. Cimatori, Hassan A. Khalid, Chebyshev Partition-Based Modulation Technique Applied to Power Converters, *IEEE Journal of Emerging and Selected Topics in Power Electronics*, Vol. 7, Issue 1, March 2019, p. 414-421.
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- [16] C. Buccella; M. G. Cimatori; V. Patel; A. M. Saif; M. Tinari; C. Cecati; E. Babaei, Harmonic elimination procedure for cascaded multilevel inverters having a particular even number of dc sources, *IECON 2018 - 44th Annual Conference of the IEEE Industrial Electronics Society*, 21-23 October 2018, Washington, DC, USA.
- [17] V. Castiglia; R. Miceli; G. Schettino; M. G. Cimatori; C. Buccella; C. Cecati, Selective Harmonic Elimination in a 5-Level Single Phase Converter with FPGA Based Controller, 2018 5th International Symposium on Environment-Friendly Energies and Applications (EFEA), 24-26 October 2018, Rome, Italy.
- [18] C. Buccella; C. Cecati; M.G. Cimatori; A. Damiano; S. Korjani; M. Porru; A. Serpi, *A Cascade Multilevel Configuration for Commercial Transport Aircraft*, 2018 AEIT International Annual Conference, Bari, 3-5 Ottobre 2018.
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- [20] F. Chabni; R. Taleb; M. G. Cimatori; C. Buccella, Selective Harmonic Elimination Procedure for Uniform Step Asymmetrical 7-Level CHB Inverter, 2018 5th International Symposium on Environment-Friendly Energies and Applications (EFEA), 24-26 September 2018, Rome, Italy.
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- [23] C. Buccella, C. Cecati, M.G. Cimatorni, G. Kulothungan, A. Edpugnanti, A. K. Rathore, *A Selective Harmonic Elimination method for Multilevel Converters for Distributed generation*. IEEE Journal of Emerging and Selected Topics in Power Electronics, June 2017, Vol. 5, issue 2, p. 775-783.
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- [27] C. Buccella, C. Cecati, M.G. Cimatorni, *SHE formulation for five level inverters with unequal DC sources*. International Conference on Industrial Technology (ICIT 2015), Seville, 17-19 March 2015, p. 1167-1172.
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